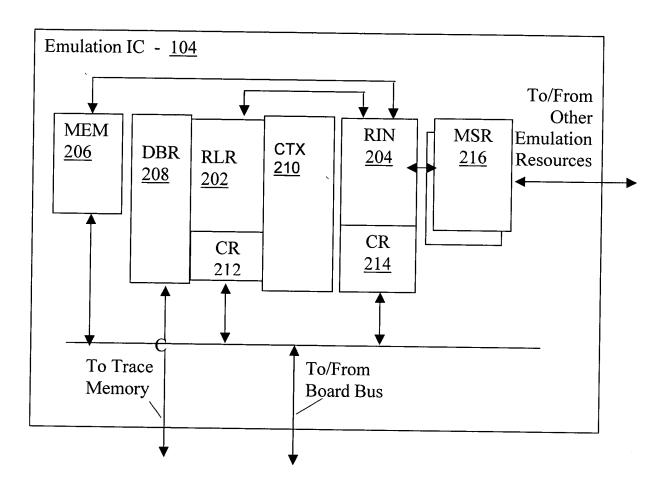


Figure 1



Key:

RLR = Reconfigurable Logic Resources

RIN = Reconfigurable Interconnect Network

MEM = Memory

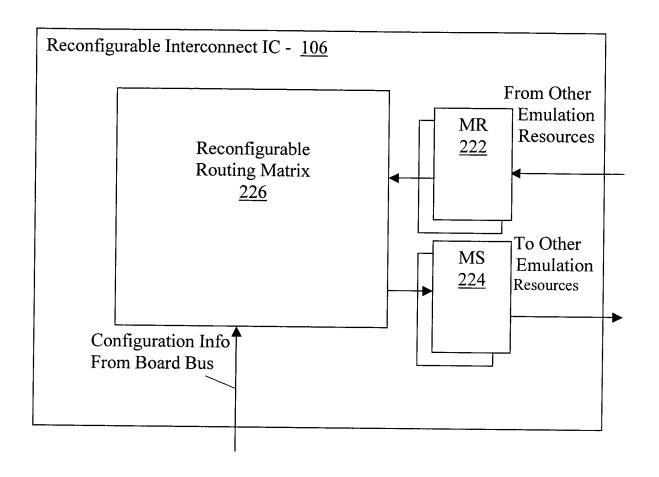
DBR = Debugging Resources

CR = Configuration Registers

CTX = Context/State Element

MSR = Message Send and Receive

Figure 2a



Key:

MS = Message Send

MR = Message Receive

Figure 2b

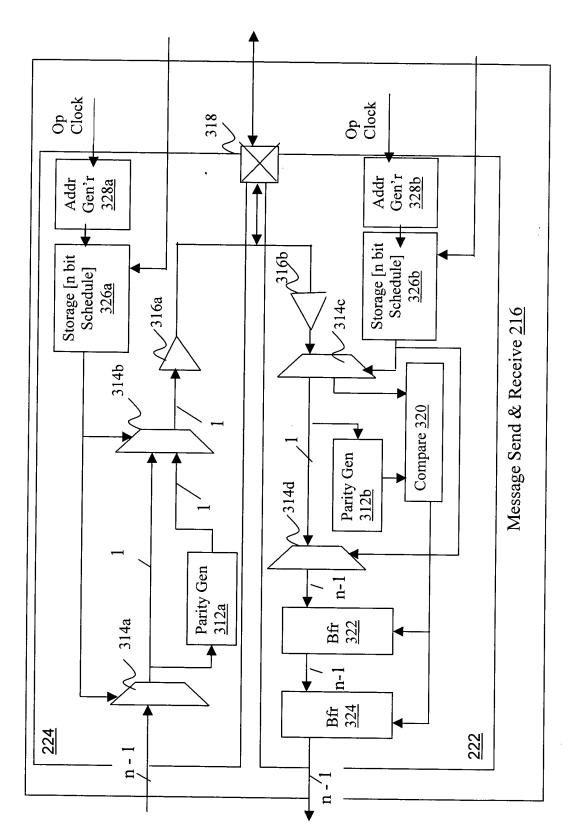


Figure 3

	A
	В
	A
	С
	A
	В
	A
	D
	A
	В
	A
C	
	A
В	
A	
parity value	

Figure 4

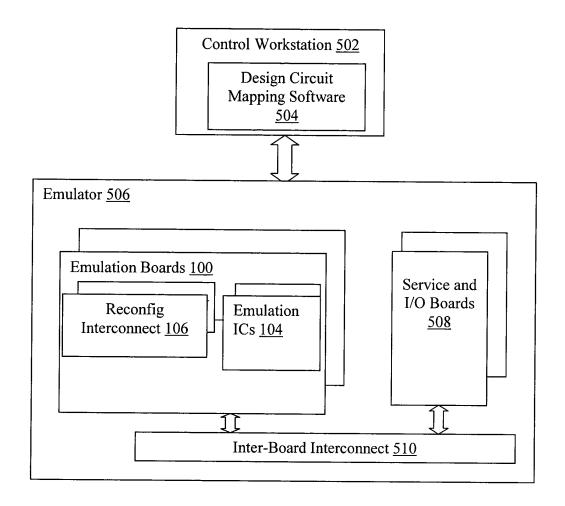
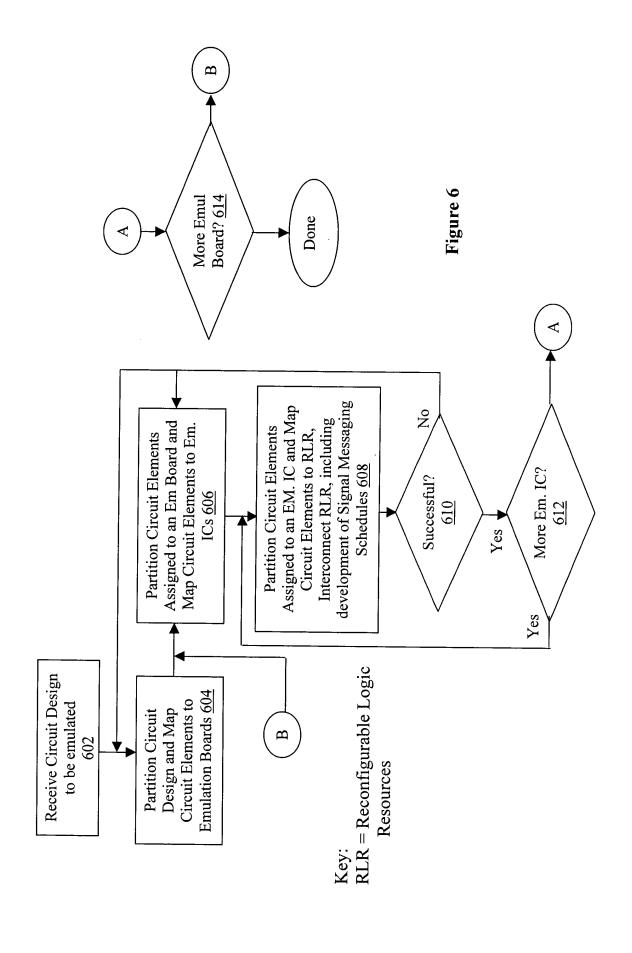


Figure 5



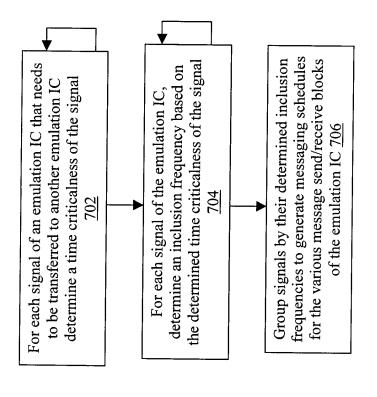


Figure 7